

WHAT IS CLAIMED IS:

1. An image display device comprising:
 - a pixel portion comprising a display region and a testing region, wherein the
 - 5 display region and the testing region comprises a first pixel circuit and a second pixel circuit, respectively;
 - each of the first and second pixel circuits comprising:
 - a data signal line;
 - a scanning line;
 - 10 a driver transistor; and
 - a switching transistor provided between the data signal line and a gate electrode of the driver transistor, wherein a gate electrode of the switching transistor is electrically connected to the scanning line,
 - wherein the driver transistor of the first pixel circuit is provided between a
 - 15 current supply line and a light emitting element, and
 - wherein the driver transistor of the second pixel circuit is provided between the current supply line and a testing terminal.
2. An image display device according to claim 1, wherein the testing region is
- 20 provided in a dummy pixel region which is provided in an outer edge region of the pixel portion.
3. An image display device comprising:
 - a pixel portion comprising a display region and a testing region, wherein the
 - 25 display region and the testing region comprises a first pixel circuit and a second pixel circuit, respectively;
 - the first pixel circuit comprising:
 - a data signal line;
 - a scanning line;
 - 30 a driver transistor provided between a current supply line and a light emitting

element; and

a switching transistor provided between the data signal line and a gate electrode of the driver transistor; and

the second pixel circuit comprising:

5 the data signal line;

the scanning line; and

the switching transistor provided between the data signal line and a testing output terminal,

10 wherein a gate electrode of the switching transistor of the first pixel circuit is electrically connected to the scanning line.

4. An image display device according to claim 3, wherein the testing region is provided in a dummy pixel region which is provided in an outer edge region of the pixel portion.

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5. An image display device comprising:

a pixel portion comprising a display region and a testing region, wherein the display region and the testing region comprises a first pixel circuit and a second pixel circuit, respectively;

20 each of the first and second pixel circuits comprising:

a data signal line;

a scanning line;

a driver transistor; and

25 a switching transistor provided between the data signal line and a gate electrode of the driver transistor, wherein a gate electrode of the switching transistor is electrically connected to the scanning line,

wherein the driver transistor of the first pixel circuit is provided between a current supply line and a light emitting element,

30 wherein the driver transistor of the second pixel circuit is provided between the current supply line and a testing terminal, and

wherein the scanning line is placed in a selected state to provide a conduction to the switching transistor in the second pixel circuit and a drain current of the driver transistor is outputted to the testing output terminal according to the signal outputted to the data signal line.

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6. A method for testing an image display device according to claim 5, wherein a signal outputted to the data signal line is a video signal.

7. A method for testing an image display device according to claim 5, wherein a
10 signal outputted to the data signal line is a testing pulse.

8. An image display device comprising:

a pixel portion comprising a display region and a testing region, wherein the display region and the testing region comprises a first pixel circuit and a second pixel
15 circuit, respectively;

the first pixel circuit comprising:

a data signal line;

a scanning line;

a driver transistor provided between a current supply line and a light emitting
20 element; and

a switching transistor provided between the data signal line and a gate electrode of the driver transistor;

the second pixel circuit comprising:

the data signal line;

25 the scanning line; and

the switching transistor provided between the data signal line and a testing output terminal,

wherein a gate electrode of the switching transistor of the first pixel circuit is electrically connected to the scanning line, and

30 wherein the scanning line is placed in a selected state to provide a conduction to

the switching transistor in the second pixel circuit and output the signal which is outputted to the data signal line to the testing output terminal.